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12/04/01

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

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**APPLICANTS: Kudo Mamoru; Nakamura Shinobu;

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2653

2655

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

JAPAN P2000-374695 12/05/2000

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

SON-2274

TITLE : Phase-locked loop circuit for reproducing A channel lock

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

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|---|-----------|-----------------------------|----------------------|
| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Total Claims | Print Claim for O.G. |
| ISSUE FEE | | DRAWING | |
| Amount Due | Date Paid | Sheets Drwg. | Figs. Drwg. |
| | | Print Fig. | |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | Application Examiner | |
| | | PREPARED FOR ISSUE | |
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